

With E1 or V.35 Interface

## PRODUCT OVERVIEW

TDM over IP is designed as a multiservice access platform for PDH and V.35 over Ethernet applications. Structured/unstructured E1 and V.35 data can be mapped/de-mapped into/ from Ethernet packets. An adaptive clock recovery method for Ingress PDH (PSN -> TDM) clock generation is implemented to support E1 (ITU-TG.824) Jitter performance.

## COST-EFFECTIVE LAN DEPLOYMENT (PDH AND V.35 OVER ETHERNET)

TDM Over IP provides cost-effective applications of traditional circuit-switched system over Ethernet. With TDM Over IP, it is easy to interconnect with existing phone systems and V.35 over Ethernet that are used to carry data, voice and video.

## TRANSPARENT TRANSMISSION

TDM Over IP can transparently transport proprietary signaling that are required to support PBX features, including call conference, call forwarding and SS7. Customer can easily apply and enjoy better integration of TDM, V.35 and Ethernet devices with lower network expense.

## BYPASS INTERNATIONAL TOLL

With a pair of TDM Over IP and guaranteed internet bandwidth, it is sure to save cost dramatically, and to ensure the QoS of voice based on interconnections of TDM telecommunications equipment.

## FEATURES

- Support IETF RFC4533 Structure-Agnostic TDM over Packet (SAToP), Metro Ethernet Forum MEF8.
- One E1 NRZ Serial Interface with LOS/AIS detection.
- One V.35 (Nx64K) interface.
- Use Raw Encapsulation method for PDH payload over Ethernet packet.
- Support Circuit Emulation Service over Ethernet (CESoE) transport over Ethernet networks.
- Comply with IETF draft standard for CESoPSN and SAToP; Metro Ethernet Forum MEF8 IA.
- Support both Point-to-Point and Point-to-Multipoint operation.
- Support Adaptive Clock recovery block for Ingress PDH (PSN ->TDM) clock generation. Recovered clock jitter is compliant to ITU-TG.824 (E1 Jitter Control).
- configurable jitter buffer depth to compensate up to 40ms of Packet Delay Variation.
- Lost packets processing/compensation via PW (Pseudo Wire) control field Sequence Number.
- Provide Subscriber side Data traffic bandwidth control to guarantee enough TDM payload bandwidth.
- PDH LOS detection triggered PW L field or payload AIS generation at Egress direction (TDM -> PSN).
- Configurable IEEE 802.3 DA/SA assignment.

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## SPECIFICATIONS

### User Interface (CPE SIDE)

- Port: 1x E1 (ITU-T G.703) and 1x V.35.
- Interface: RJ-48c (120 Ohm), BNC (75Ohm) and M/34 female (V.35, DCE).
- E1 Line Coding: HDB3.

### Ethernet Interface (CPE / CO SIDE)

- Port: 2x 100 Base-T Ethernet. One is for downlink and the other is for uplink.
- Interface: RJ-45

### Dimensions

- H x W x D: 44 x 370 x 215 (mm)

### Main Power Supply

- AC: 110 ~ 240V @ 47 ~ 65Hz
- DC: -72V ~ -36V (Option)

### Environment Condition

- Ambient Temperature: 0°C~50°C (0°C~65°C, optional)
- Storage temperature: 0°C~ 85°C
- Relative humidity: 5 ~ 95% non condensing

### Configuration & Management

- RS-232 console port (Craft Terminal) or SNMP-based management

